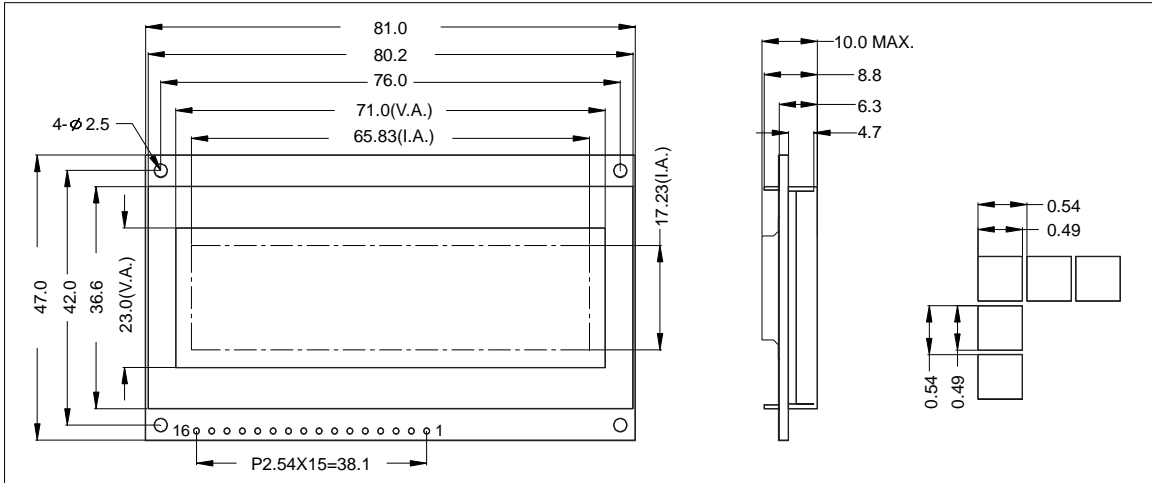


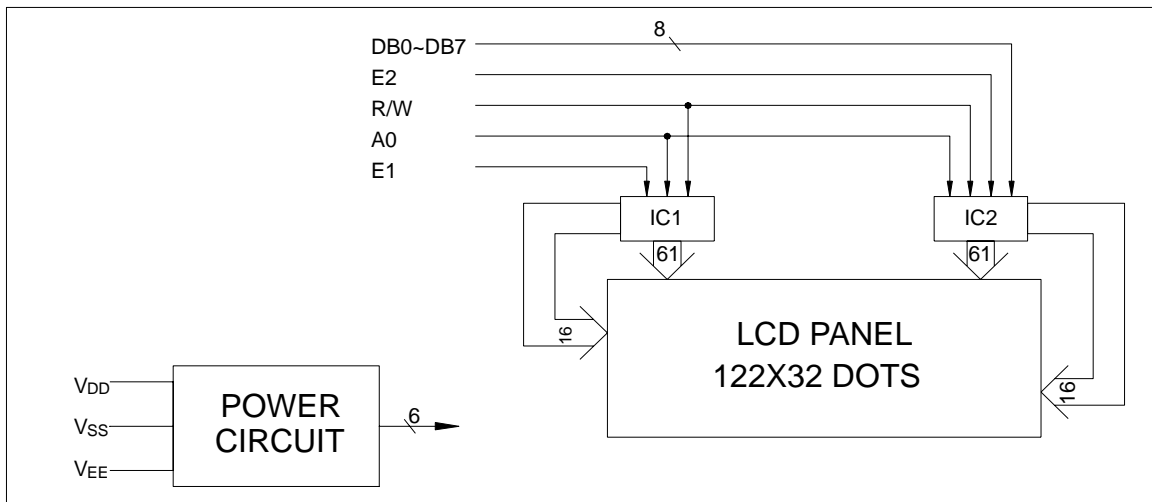
1.0 Features

- * Display Mode: Reflective/Transflective/Transmissive and Positive Type STN
- * Input Data: 8-Bits Parallel Data Input from a MPU
- * Assembly: SMT
- * Backlight: Optional

2.0 External Dimensions



3.0 Block Diagram



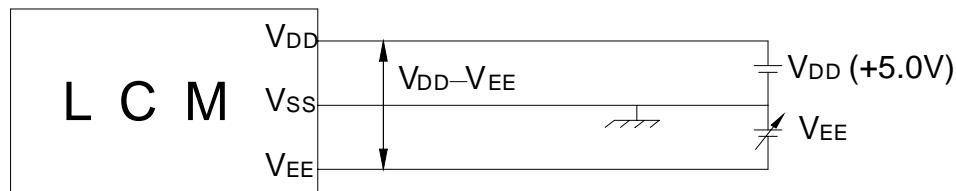
4.0 Maximum Rating

Item	Symbol	Test Condition	Standard Value		Unit
			Min.	Max.	
Supply Voltage for Logic	VDD-VSS	Ta=25 °C	0	7.0	V
Supply Voltage for LCD	VDD-VEE		0	10.0	V
Input Voltage	VI		0	VDD	V
Operating Temperature	Topr	—	0	+50	°C
Storage Temperature	Tstg	—	-10	+60	°C

5.0 Electro-Optical Characteristics

Item	Symbol	Conditions	Standard Value			Unit
			Min.	Typ.	Max.	
Power Supply for Logic	Logic	V _{DD}	4.5	5.0	5.5	V
	LCD Drive	V _{DD} -V _{EE}	—	—	6.0	
Frame Frequency	f FLM	V _{DD} =5.0V	65	70	75	Hz
Current Consumption	I _{DD}	V _{DD} =5.0V, V _{DD} -V ₀ =5.0V V/R=160Kohm	—	—	2.0	mA
LCD Driving Voltage(Recommended)	V _{DD} -V ₀	T _a =25 °C; Ø, θ=0°	—	5.0	—	V
Response Time(Rising)	T _r	T _a =25 °C; Ø, θ=0°	—	200	250	ms
Response Time(Decay)	T _d		—	250	300	ms
Viewing Angle	Ø2-Ø1	K≥2	-10	—	45	DEG.
Contrast Ratio	K	Ø=0°, θ=0°	2.0	5.0	—	—

6.0 Power Supply for LCM



V_{DD} - V_{EE}: LCD Driving Voltage

7.0 I/O Connection

Pin No	Symbol	Level	Function
1	A0	H/L	H: Data L: Instruction code
2	E2	H, H/L	Chip enable signal 2
3	R/W	H/L	H; Read L: Write
4	RST	L	Reset signal
5	E1	H,H/L	Chip enable signal 1
6	V _{SS}	0V	Ground
7	V _{EE}	—	Power supply for LCD
8	V _{DD}	5.0V	Power supply for logic and LCD
9	DB0	H/L	Data bit 0
10	DB1	H/L	Data bit 1
11	DB2	H/L	Data bit 2
12	DB3	H/L	Data bit 3
13	DB4	H/L	Data bit 4
14	DB5	H/L	Data bit 5
15	DB6	H/L	Data bit 6
16	DB7	H/L	Data bit 7